

S/N 10/612,762

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Karl H. Mauritz et al.

Examiner: Jia Lu

Serial No.: 10/612,762

Group Art Unit: 2611

Filed: June 30, 2003

Docket No.: 884.925US1

Title: METHOD FOR REDUCING DC OFFSET ERROR FOR SINGLE ENDED AND
DIFFERENTIAL SIGNALS

Assignee: Intel Corporation

Customer Number: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

This responds to the Office Action dated October 17, 2006. Please amend the above-identified patent application as follows.